Design Data

- Chris Garcia – Xygent
- Glen Allan/John Plonka – Ford
- Rich Knebel – Zeiss
- Jeff Walker – LK
- Simon Frechette – NIST
- Elsayed Orady – U of M Dearborn
- Hari Sannareddy – CAT

Design Data

- Including tolerances and features

Coordinate with AIAG

- June/July timeframe
- Tom Hendrix (Boeing) invited

MIC Letterhead

- Develop before sending letter to CAD

Letter to CAD vendors

- #1 concern by companies in attendance
- Written and sent by Consortium (MIC) management.
- After June/July meeting with AIAG

Get input from Boeing

- Tom Hendrix to see where we stand today with tolerancing and STEP protocols (AP203,214,224 …)

Create Roadmap

- Each group to create roadmap to be used for overall map and tool with funding issues (after #1 before #3)

Funding

- Development of feature tolerance output spec

Correctly define problem

- In proper terminology (output of coordination w/AIAG)